

FIG. 1

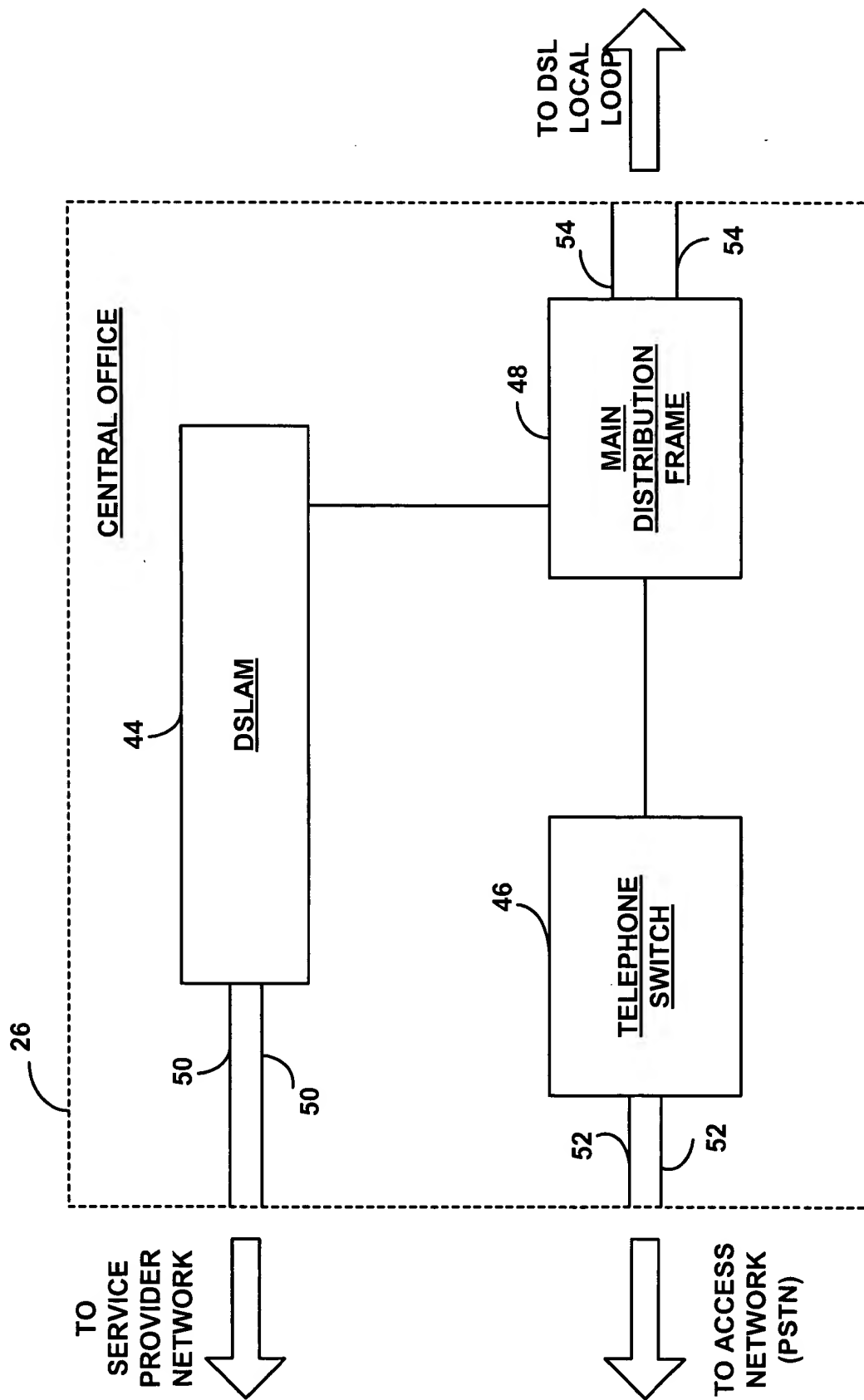


FIG. 2

44

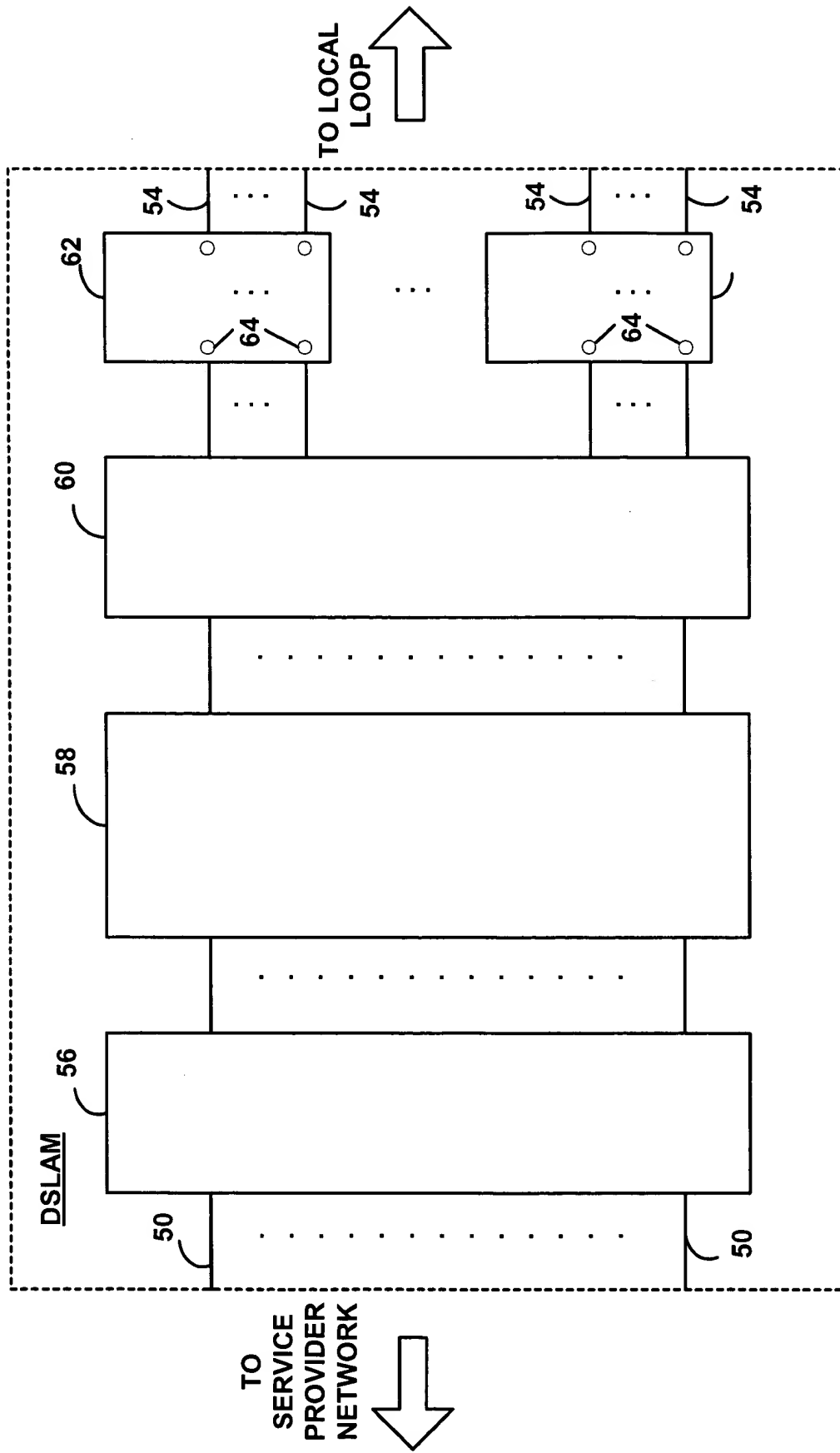


FIG. 3

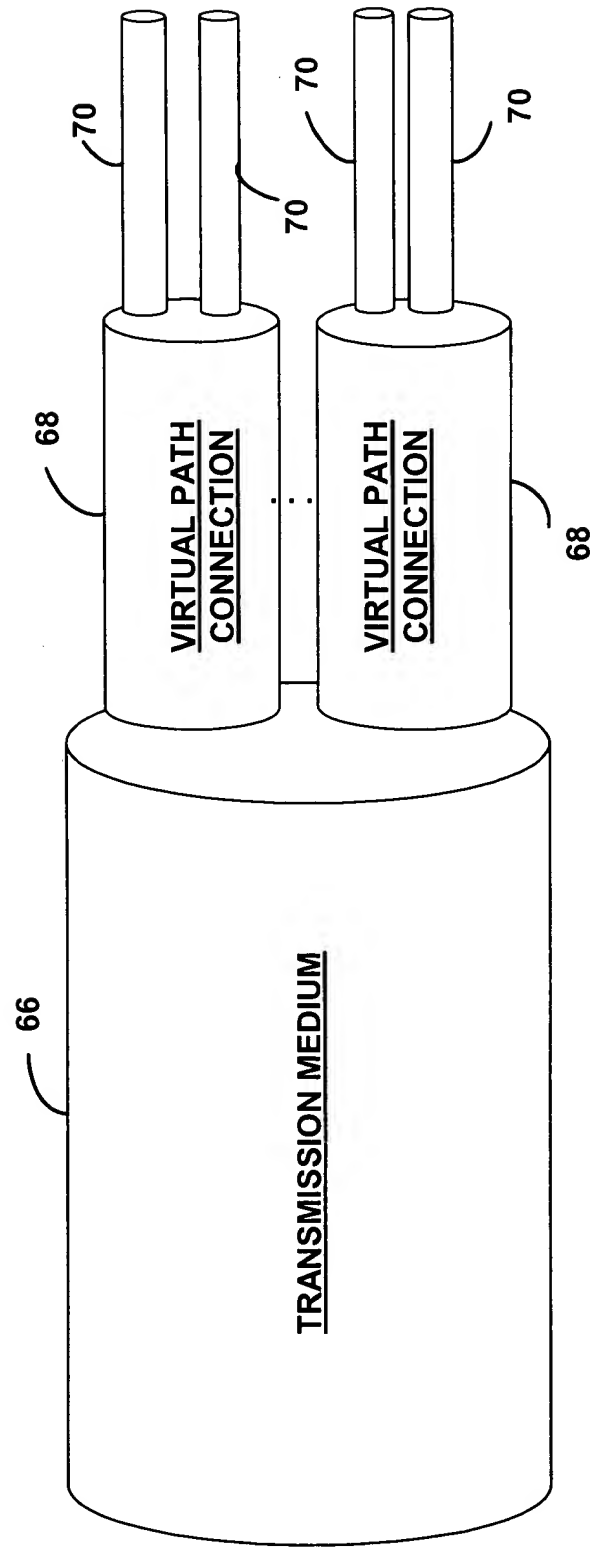


FIG. 4

Cell 1 2 3 4 5 6 7 8

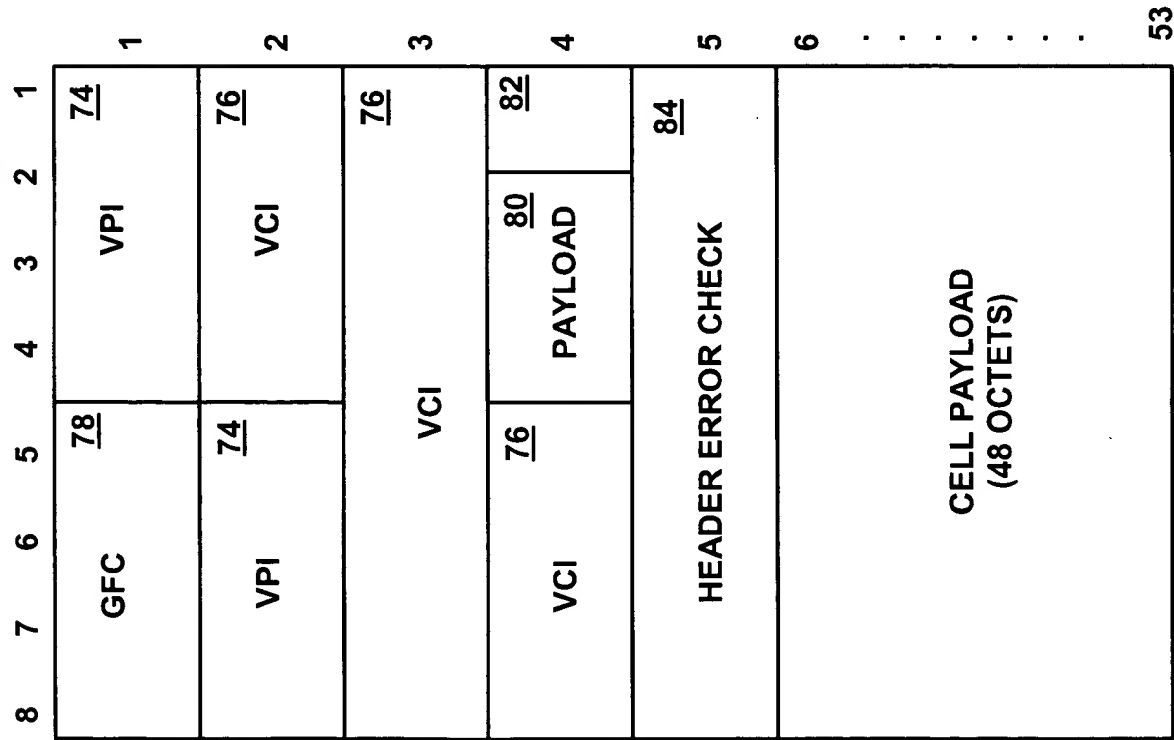


FIG. 5

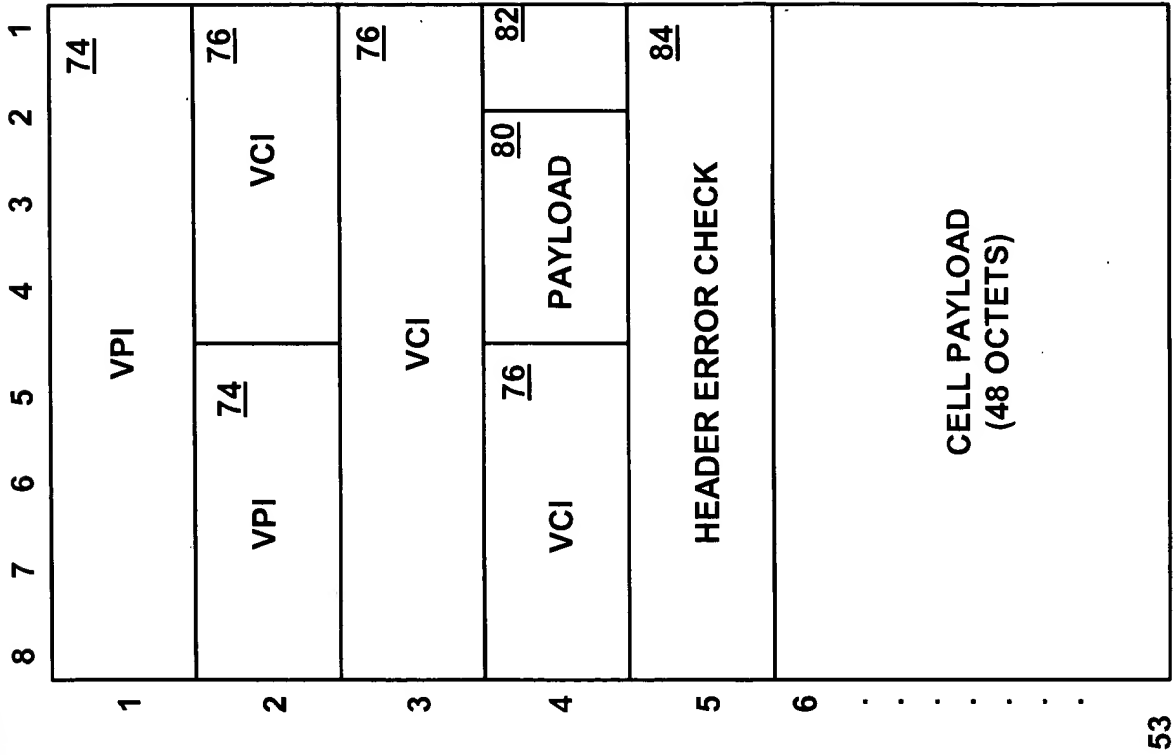


FIG. 6

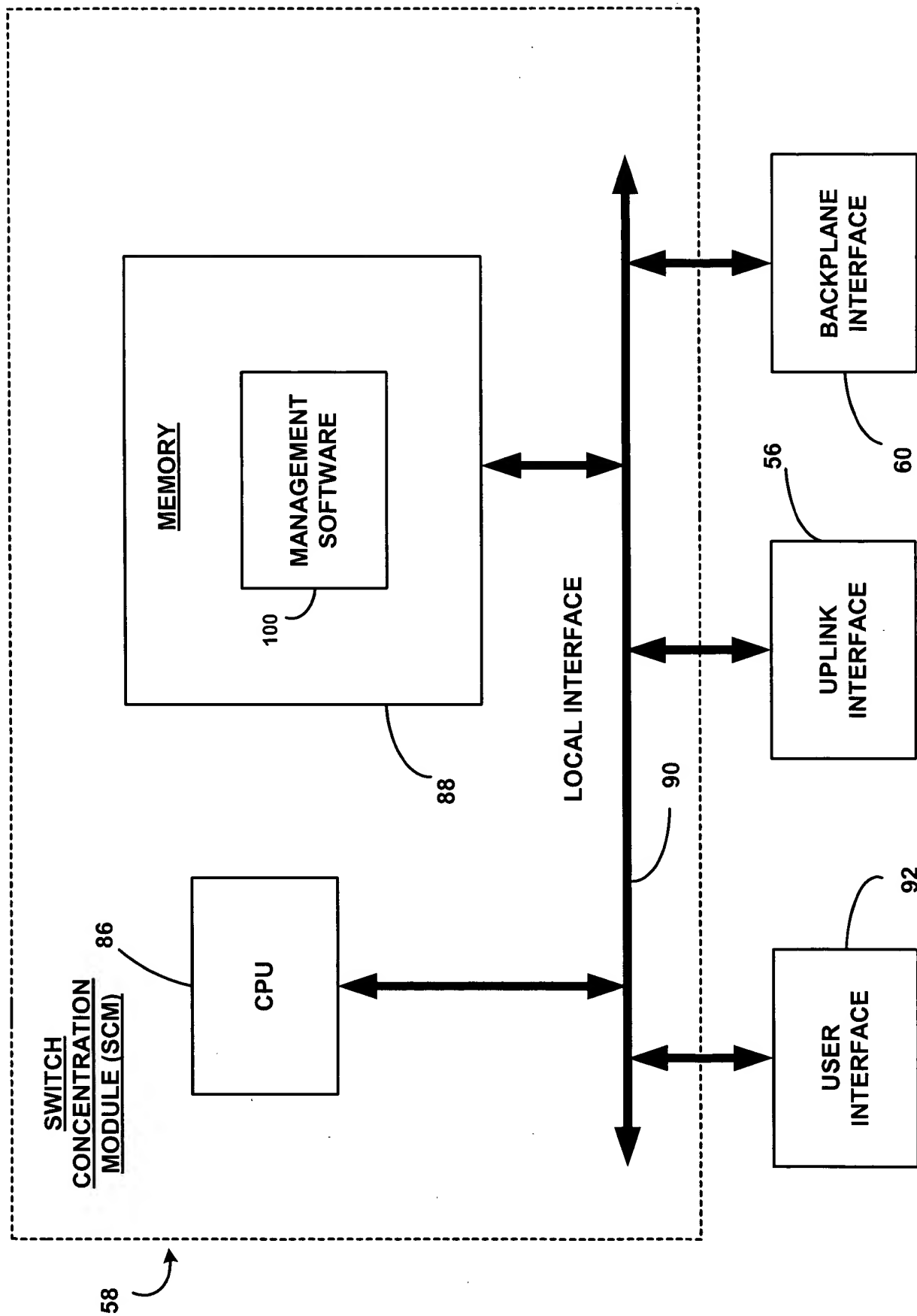
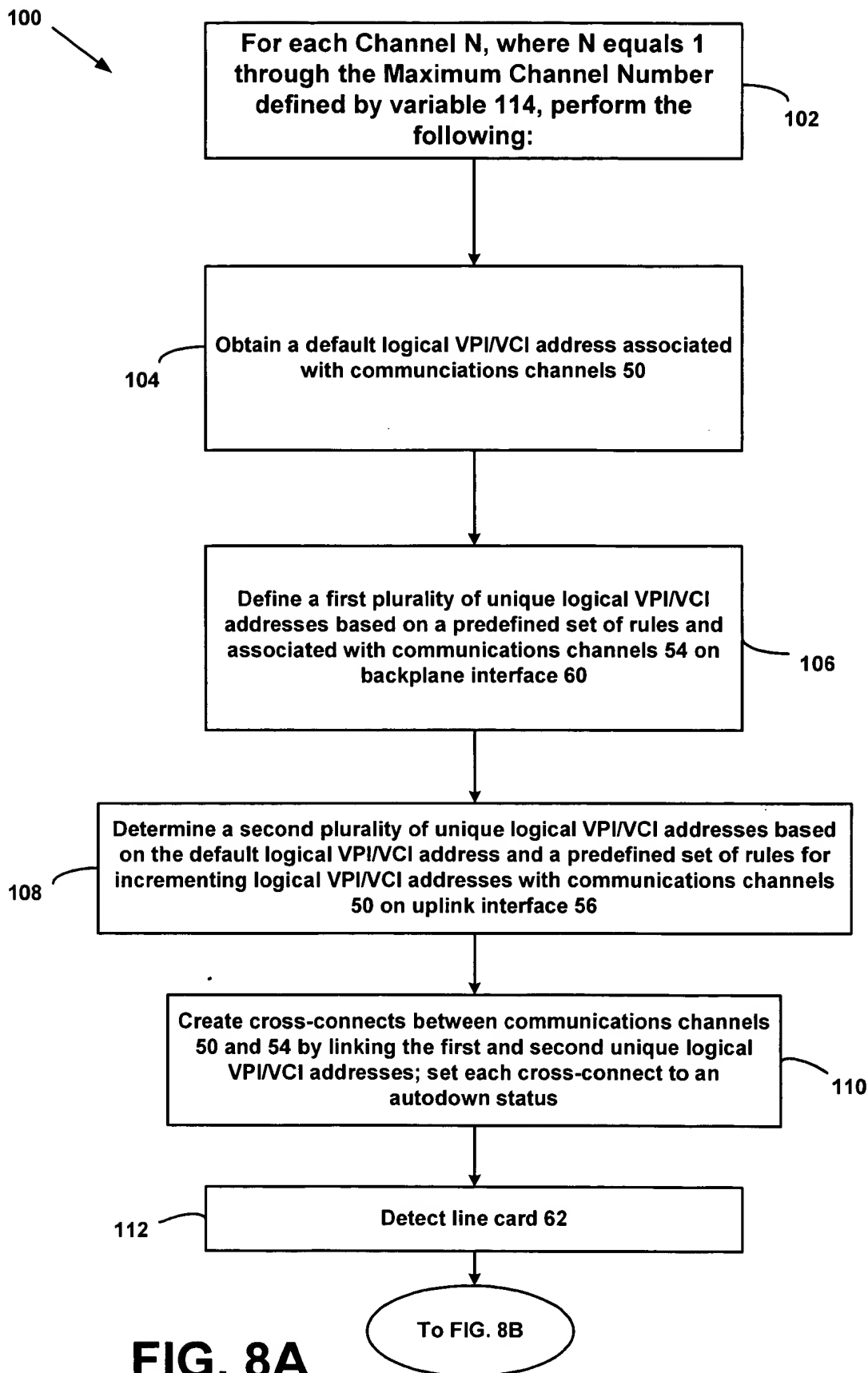


FIG. 7



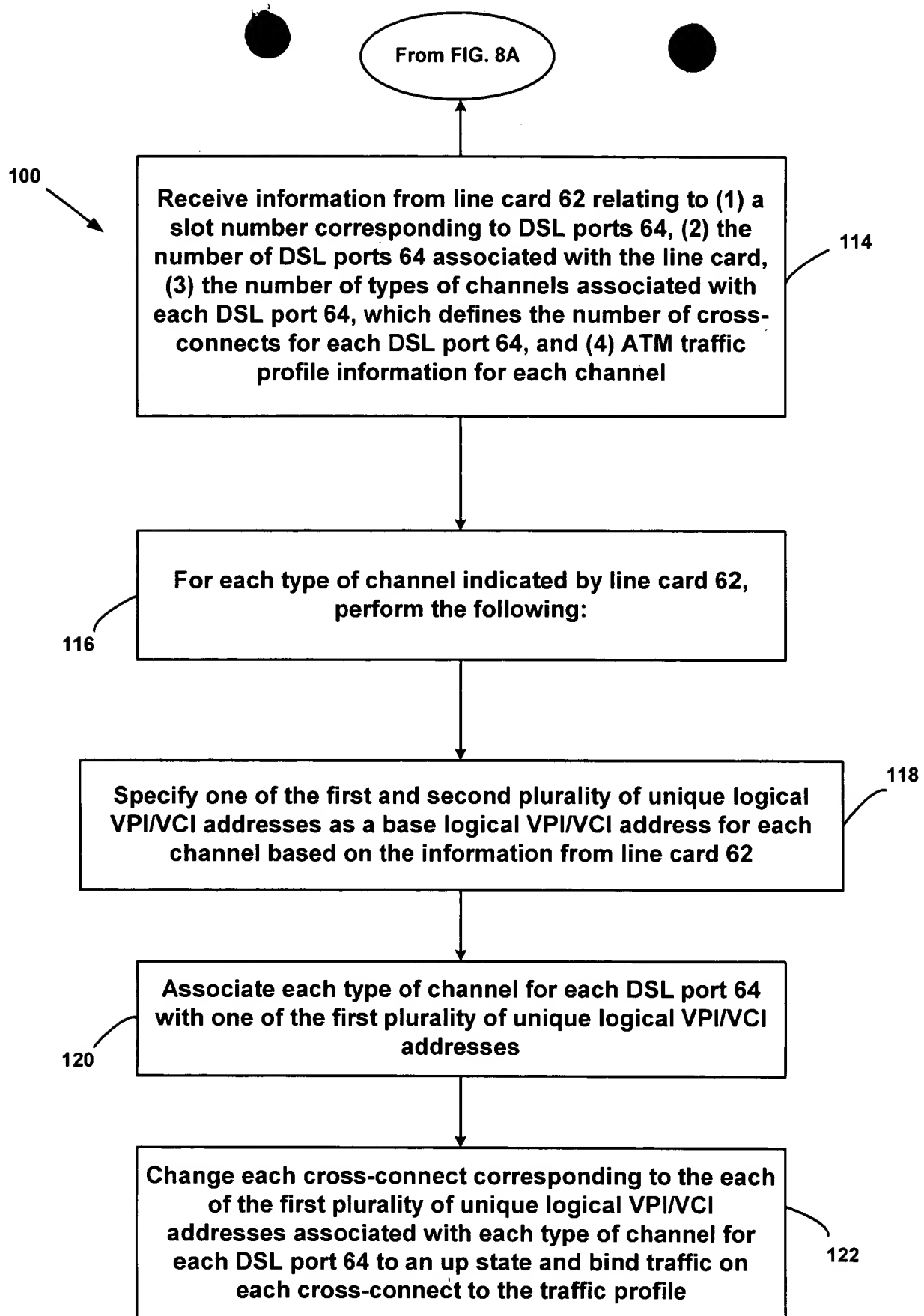


FIG. 8B

<u>144</u>	LINE CARD VARIABLE	VALUE
<u>146</u>	SLOT #	
<u>148</u>	NUMBER OF PORTS	
<u>150</u>	REQUESTED NUMBER OF CHANNELS PER PORT	
<u>152</u>	REQUESTED TRAFFIC PROFILE INDICATOR PER CHANNEL	

FIG. 9

<u>154</u>	DSL PORT VARIABLE	VALUE
<u>154</u>	DSL PORT #	
<u>156</u>	MAX VPI	
<u>158</u>	MAX VCI	
<u>160</u>	STATUS	
<u>162</u>	CONFIGURATION PARAMETERS (# channels, ATM parameters, upstream and downstream rate table, etc.)	

FIG. 10

<u>166</u>	BACKPLANE INTERFACE VARIABLE	VALUE
<u>168</u>	INTERFACE ID	
<u>170</u>	MAX VPI	
<u>172</u>	MAX VCI	
<u>174</u>	STATUS	
<u>176</u>	OTHER PARAMETERS	

FIG. 11

<u>178</u>	UPLINK INTERFACE VARIABLE	VALUE
<u>180</u>	INTERFACE ID	
<u>182</u>	MAX VPI	
<u>184</u>	MAX VCI	
<u>186</u>	STATUS	
<u>188</u>	OTHER PARAMETERS	

FIG. 12

<u>190</u>	CROSS-CONNECT VARIABLE	VALUE
<u>192</u>	CROSS CONNECT ID	
<u>194</u>	IFINDEX1	
<u>196</u>	VPI1	
<u>200</u>	VCI1	
<u>202</u>	IFINDEX2	
<u>204</u>	VPI2	
<u>206</u>	VCI2	

FIG. 13

CROSS-CONNECTION TABLE		
<u>210</u>		
<u>212</u>	<u>216</u> STATUS	<u>214</u> BACKPLANE INTERFACE: VPI: VCI
<u>UPLINK INTERFACE: VPI: VCI</u> [UPLINK INTERFACE = Ifup = 1] [VPI0 ≤ VPI ≤ VPI _m] [VCI0 ≤ VCI ≤ VCI _m] [p = number of ports per card] [c = number of cards in system]		[IF1 ≤ BACKPLANE INTERFACE ≤ IF _c] [VPI0 = fixed starting VPI] [VCI0 fixed starting VCI] [p = number of ports per card] [c = number of cards in system]
IFup: VPI0: VCI0		IF1: VPI0: VCI0
IFup: VPI0: VCI0+1		IF1: VPI0+1: VCI0
IFup: VPI0: VCI0+p-2		IF1: VPI0+p-2: VCI0
IFup: VPI0: VCI0+p-1		IF1: VPI0+p-1: VCI0
IFup: VPI0: VCI0+p		IF2: VPI0/ VCI0
IFup: VPI0: VCI0+p+1		IF2: VPI0+1: VCI0
IFup: VPI0: VCI0+p*2-2		IF2: VPI0+p-2: VCI0
IFup: VPI0: VCI0+p*2-1		IF2: VPI0+p-1: VCI0
IFup: VPI0: VCI0+p*(c-2)		IF2: VPI0/ VCI0
IFup: VPI0: VCI0+p*(c-2)+1		IF2: VPI0+1: VCI0

FIG. 14A

CROSS-CONNECTION TABLE		
<u>210</u>		
<u>212</u>	<u>UPLINK INTERFACE:VPI:VCI</u> [UPLINK INTERFACE = Ifup = 1] [VPI0 ≤ VPI ≤ VPI _m] [VCI0 ≤ VCI ≤ VCI _m] [p = number of ports per card] [c = number of cards in system]	<u>216</u> <u>STATUS</u>
<u>214</u>	<u>BACKPLANE INTERFACE:VPI:VCI</u> [IF1 ≤ BACKPLANE INTERFACE ≤ IFc] [VPI0 = fixed starting VPI] [VCI0 fixed starting VCI] [p = number of ports per card] [c = number of cards in system]	
	IFup:VPI0:VCI0+p*(c-1)-2	IFc:VPI0+p-2:VCI0
	IFup:VPI0:VCI0+p*(c-1)-1	IFc:VPI0+p-1:VCI0
	IFup:VPI1:VCI1	IF1:VPI0:VCI1
	IFup:VPI1:VCI1+1	IF1:VPI0+1:VCI1
	IFup:VPI1:VCI1+p-2	IF1:VPI0+p-2:VCI1
	IFup:VPI1:VCI1+p-1	IF1:VPI0+p-1:VCI1
	IFup:VPI1:VCI1+p	IF2:VPI0/ VCI1
	IFup:VPI1:VCI1+p+1	IF2:VPI0+1:VCI1
	IFup:VPI1:VCI1+p*(c-2)	IF2:VPI0/ VCI1
	IFup:VPI1:VCI1+p*(c-2)+1	IF2:VPI0+1:VCI1

FIG. 14B

CROSS-CONNECTION TABLE		
<u>210</u>		
<u>212</u>	<u>216</u> STATUS	<u>214</u> BACKPLANE INTERFACE:VPI:VCI
<u>UPLINK INTERFACE:VPI:VCI</u> [UPLINK INTERFACE = Ifup = 1] [VPI0 ≤ VPI ≤ VPIIm] [VCI0 ≤ VCI ≤ VCIm] [p = number of ports per card] [c = number of cards in system]		[IF1 ≤ BACKPLANE INTERFACE ≤ IFc] [VPI0 = fixed starting VPI] [VCI0 fixed starting VCI] [p = number of ports per card] [c = number of cards in system]
	Ifup:VPI1:VCI1+p*(c-1)-2	IFc:VPI0+p-2:VCI1
	Ifup:VPI1:VCI1+p*(c-1)-1	IFc:VPI0+p-1:VCI1
	Ifup:VPIIm:VCIIm	IF1:VPI0:VCIc-1
	Ifup:VPIIm:VCIIm+1	IF1:VPI0+1:VCIc-1
	Ifup:VPIIm:VCIIm+p-2	IF1:VPI0+p-2:VCIc-1
	Ifup:VPIIm:VCIIm+p-1	IF1:VPI0+p-1:VCIc-1
	Ifup:VPIIm:VCIIm+p	IF2:VPI1/ VCIc-1
	Ifup:VPIIm:VCIIm+p+1	IF2:VPI2:VCIc-1
	Ifup:VPIIm:VCIIm+p*(c-2)	IF2:VPI0/ VCI0
	Ifup:VPIIm:VCIIm+p*(c-2)+1	IF2:VPI0+1:VCI0

FIG. 14C

<u>220</u>	VCL VARIABLE	VALUE
<u>222</u>	IFINDEX	
<u>224</u>	VPI	
<u>226</u>	VCI	
<u>228</u>	TRAFFIC PROFILE UP	
<u>230</u>	TRAFFIC PROFILE DOWN	

FIG. 15

<u>232</u>	AUTO-CONFIGURATION RECORD	
	AUTO-CONFIGURATION VARIABLE	VALUE
<u>234</u>	INTERFACE ID	
<u>236</u>	CHANNEL	
<u>238</u>	BASE VPI	
<u>240</u>	BASE VCI	

FIG. 16

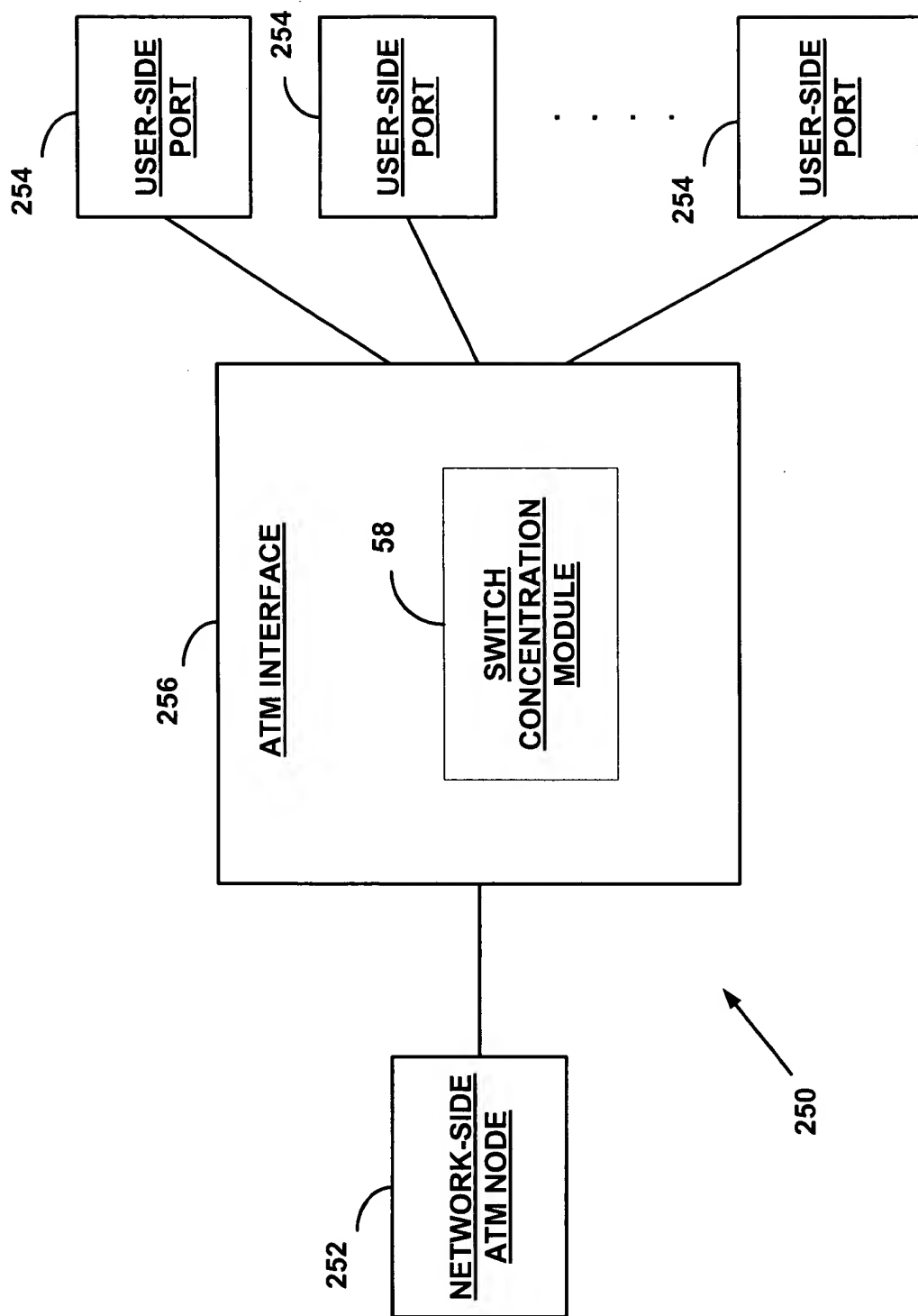


FIG. 17

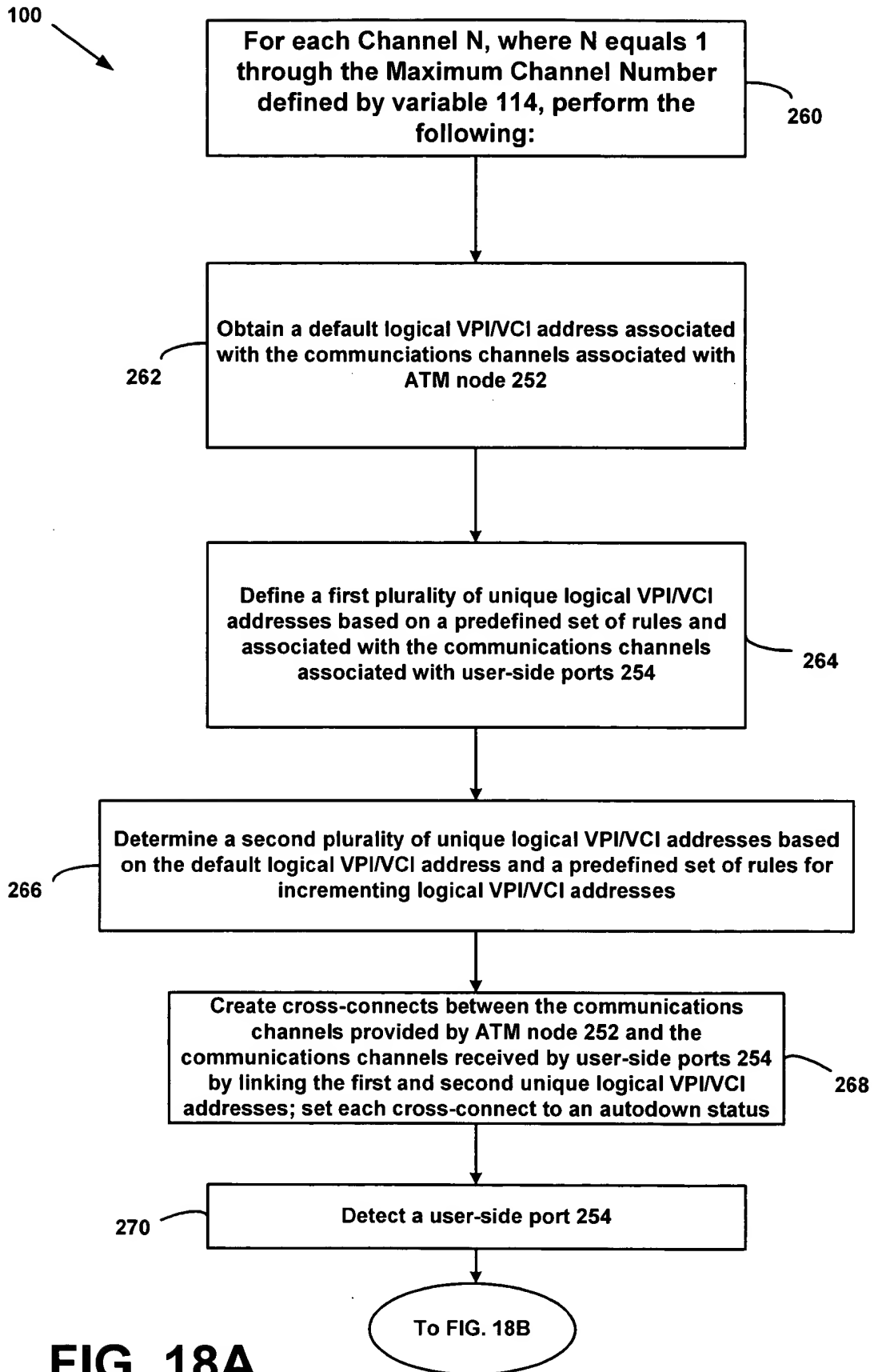


FIG. 18A

100

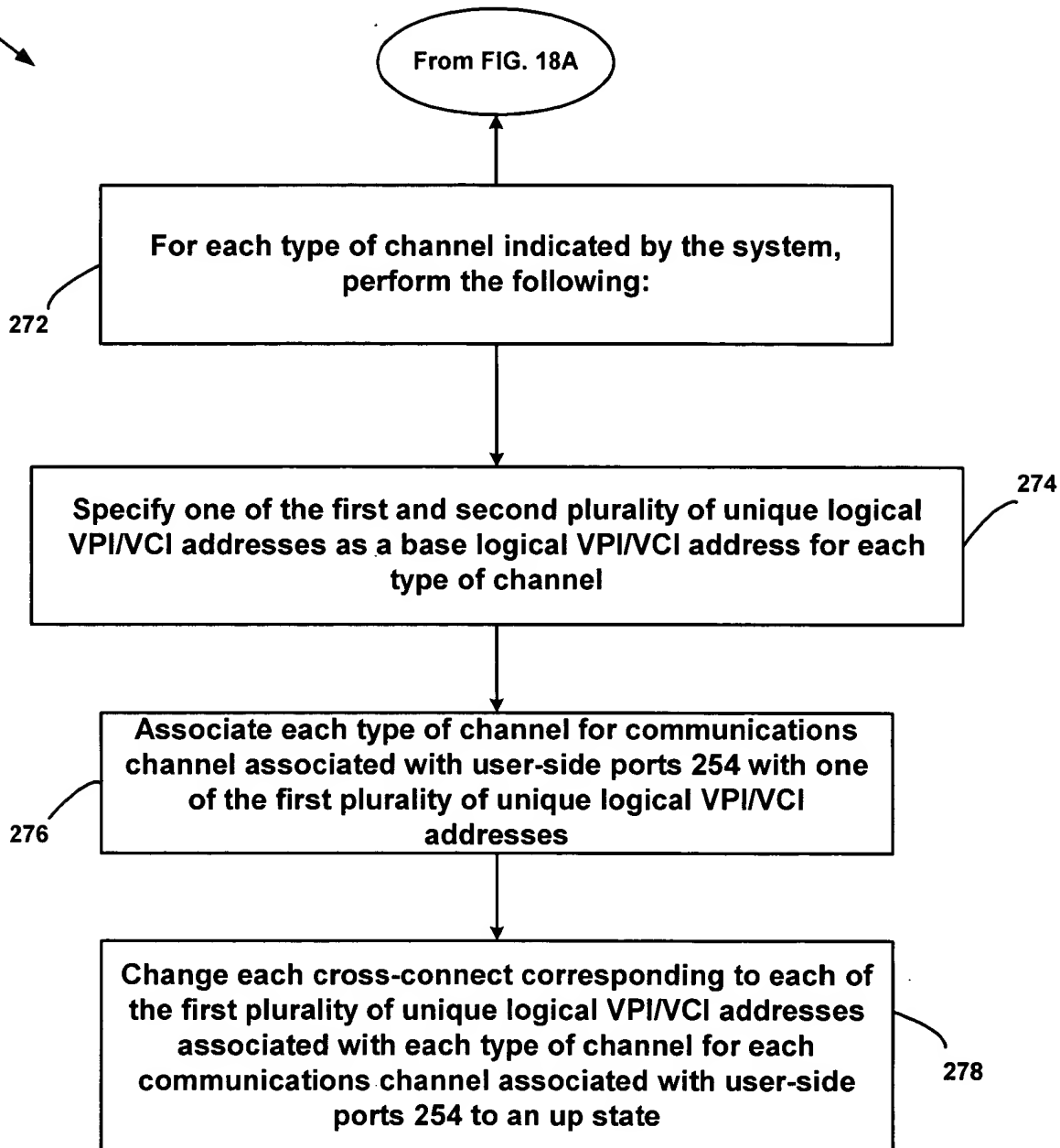


FIG. 18B